# 2SK1575

## Silicon N-Channel MOS FET

# HITACHI

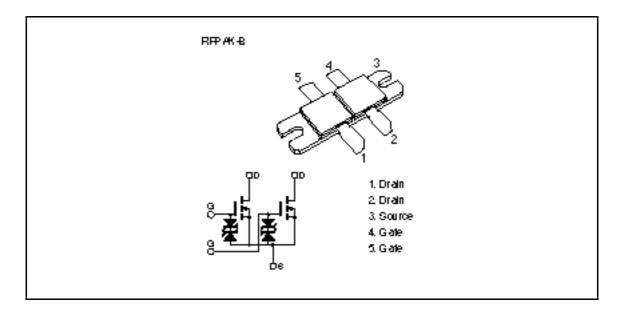
#### **Application**

VHF amplifier

#### **Features**

- High gain, high efficiency  $PG=13\ dB,\quad D=65\%\ typ\ (f=190\ MHz)$
- Compact package Suitable for push - pull circuit

#### **Outline**





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### Absolute Maximum Ratings ( $Ta = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit	
Drain to source voltage	$V_{ t DSS}$	180	V	
Gate to source voltage	$V_{\sf GSS}$	±20	V	
Drain current	I <sub>D</sub>	16	A	
Channel dissipation	Pch*1	200	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

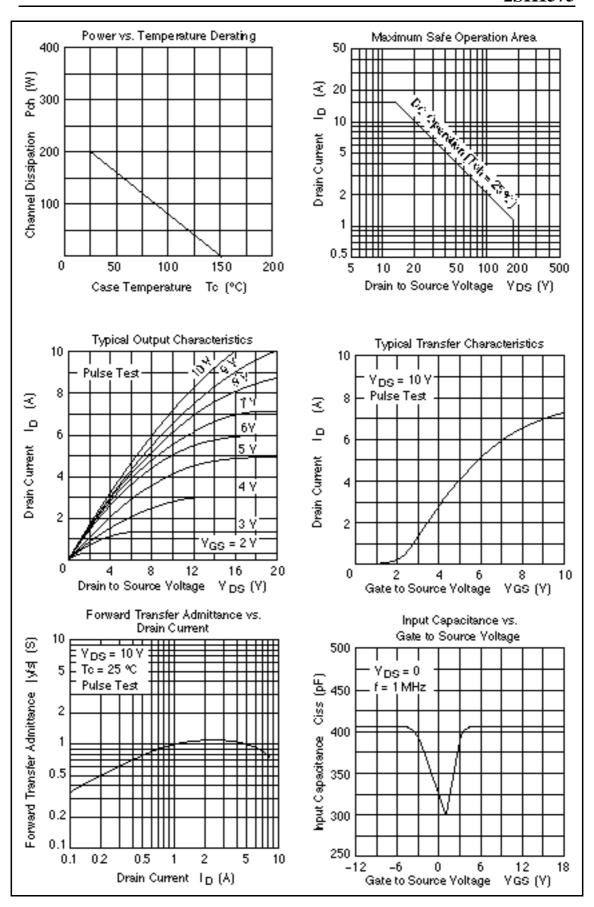
Note: 1. Value at  $T_c = 25^{\circ}C$ 

### **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

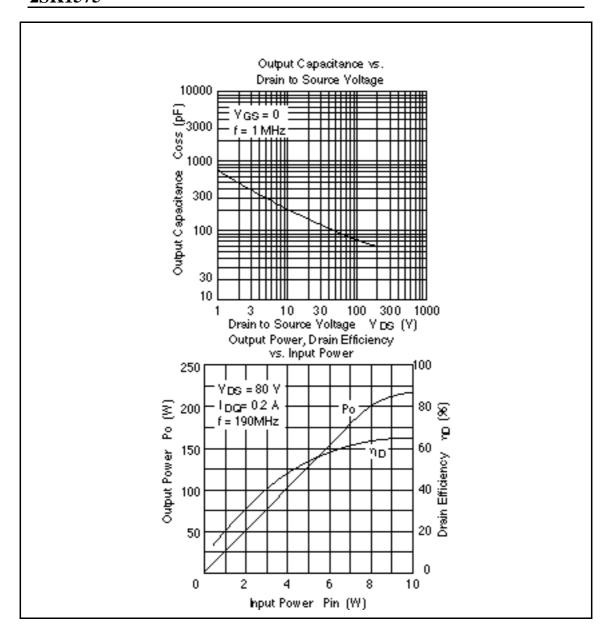
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage*1	$V_{(BR)DSS}$	180	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage*1	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current*1	I <sub>DSS</sub>	_	_	1	mA	$V_{DS} = 140 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage*1	$V_{GS(off)}$	0.5	_	2.0	V	$I_{D} = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Drain to source cutoff voltage*1	$V_{\rm DS(on)}$	_	3.8	6.0	V	$I_D = 4 \text{ A}, V_{GS} = 10 \text{ V}^{*2}$
Forward transfer admittance*1	y <sub>fs</sub>	0.9	1.25	_	S	$I_D = 3 \text{ A}, V_{DS} = 20 \text{ V}^{*2}$
Input capacitance*1	Ciss	_	440	_	pF	$V_{GS} = 5 \text{ V}, V_{DS} = 0$ f = 1 MHz
Output capacitance*1	Coss	_	75	_	pF	$V_{DS} = 50 \text{ V}, V_{GS} = 0$ f = 1 MHz
Reverse transfer capacitance*1	Crss	_	0.5	_	pF	$V_{GD} = -50 \text{ V}, f = 1 \text{ MHz}$
Output Power	Po	180	220	_	W	$V_{DS} = 80 \text{ V}, I_{DQ} = 0.2 \text{ A}$
Drain Efficiency	D	_	65	_	%	 f = 190 MHz, Pin = 10 W

Notes: 1. Shows / unit FET

2. Pulse Test

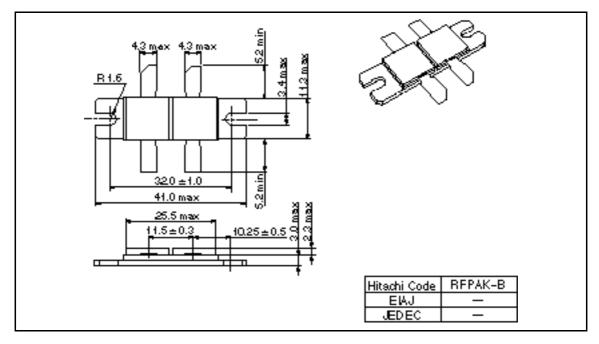


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### **Package Dimensions**

Unit: mm



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